

PE-10S

**PICMG 1.3 10 Slots Backplane
with PCI-E X16, PCI-E X4, PCI-E X1 and PCI Slots**

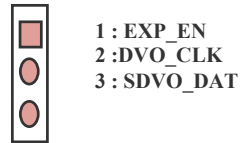
Quick Installation Guide

Version 1.0

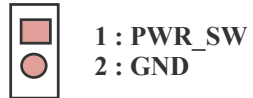
Apr. 01, 2006

Jumpers setting

CN1
(ATX 5VSB/PS on Pin Header) **CN2**
(3Pin SDVO Control Signal Pin Header)



JP1
(Power Switch)



JP2(Reset)



JP3
(PCI-E Link Configurations)

Jumper short Pin1 and Pin2 : PCIE2 ~ PCIE5 = x1
Jumper short Pin2 and Pin3 : PCIE2 ~ PCIE4 = Not available
PCIE5 = x4

JP3	PCIE2	PCIE3	PCIE4	PCIE5
1-2	PCIEx1	PCIEx1	PCIEx1	PCIEx1
2-3	N/A	N/A	N/A	PCIE4



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